P1500D1R.D1 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Youichi Imamura

Group Art Unit:

Not Yet Assigned

Serial No.:

Unknown

Examiner:

Not Yet Assigned

Filed:

Herewith

Title:

Display And Display Driver With On/Off Power Controller Used To

Prevent Damage To The Display

## **CERTIFICATION UNDER 37 CFR 1.10**

"Express Mail" Mailing Label Number: EV311301659US

I hereby certify that this Information Disclosure Statement, and the documents referred to as enclosed therein are being deposited with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee" under 37 CFR 1.10 on the date indicated below and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450".

Dated: October 1, 2003

INFORMATION DISCLOSURE STATEMENT

Mail Stop Reissue Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. §1.56, and in accordance with the practice under 37 C.F.R. §1.97 and §1.98, the Examiner's attention is directed to the document(s) listed on the enclosed Form PTO-1449. A copy of each listed document is enclosed, except, in the case where this application is filed after June 30, 2003, copies of any U.S. patents and U.S. patent application publications are not enclosed.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits. No statement or fee is required (37 CFR §1.97(b)).

## CONCLUSION

The Commissioner is hereby authorized to charge any additional fees, which may be required, or credit any over-payment to Deposit Account No.: 19-2746.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Respectfully submitted,

Mark P. Watson

Registration No. 31,448

Please address all correspondence to:

Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134 Customer No. 20178 Phone: (408) 952-6000

Facsimile: (408) 954-9058

Date: October 1, 2003

Express Mail Label No.: EV311301659US

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PATENT AND TRADEM				DEMARK OF FIC	Æ	Unknown		P1500D1R.D1				
	INFORMATION DISCLOSURE CITATION					APPLICANT(S						
	(USE SEVERAL SHEETS IF NECESSARY)								Imamura GROUP			
PAGE 1 OF 2				Herewith		Not Yet Assigned						
	U.S. 1			ATENT	ATENT DOCUMENTS							
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	F	B 4,674,031 6/16/87		Siska	Siska, Jr.							
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	AI			ng of the SID. Vol. 30 (1989) No. 2, New York, "Peripheral Circuit								
	AI	THIL	integrated 1 oly-51 11 1 LCD with Gray Scale Representation, Juni-1cm On									
		al., pp. 131-136.										

**EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

DATE CONSIDERED

AS

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**EXAMINER** 

Fori	n PTO-14	19 DEPARTMENT OF C PATENT AND TRADEN			SERIAL NO. Unknown				ATTY DOCKET NO. P1500D1R.D1			
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		ORMATION DISCLOSURE			Youichi Imamura							
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E.I.		DOCUMENT NUMBER	DATE		COUNTRY		SS	SUB	CLASS			
	AL1	58-123118	7/22/83	Japan							Abstract	
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	AO1	03-202812	September 4, 1991		Japan						Yes	
	AP1	03-153294	July 1, 1991	Japan ————	Japan						Yes	
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